

PROTECTION

5 VOLT TRANSIENT VOLTAGE SUPPRESSOR «TRANSIL» FOR MICROPROCESSOR, INTEGRATED CIRCUIT C-MOS, MOS PROTECTION

Unidirectional type	$I_{RM} @ V_{RM} \text{ max}$		$V_{(BR)}^* @ I_R \text{ min}$		$V_{(CL)} @ I_{pp} \text{ max}$ 1 ms expo		$V_{(CL)} @ I_{pp} \text{ max}$ 1 ms expo		$V_{(CL)} @ I_{pp} \text{ max}$ 1 ms expo		$\alpha_T \text{ max}$	Package
	(μA)	(V)	(V)	(mA)	(V)	(A)	(V)	(A)	(V)	(A)	($10^{-4}/^\circ\text{C}$)	

1.5 KW / 1 ms expo.

$I_{FSM} = 250 \text{ A} \cdot 10 \text{ ms}$

P 1N 5908	300	5	6.0	1	7.6	30	8.0	60	8.5	120	5.7	CB-429
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* Pulse test $t_p \leq 50 \text{ ms}$ $\delta < 2\%$.
P : Preferred device.

TRANSIL ARRAY

Type	$I_{RM} @ V_{RM} \text{ max}$		$V_{BR} @ I_R \text{ min}$		$V_{CL} @ I_{pp} \text{ max}$ 8-20 μs expo		ESD expo (1)	C max (2)	Package
	(μA)	(V)	(V)	(mA)	(V)	(A)	(kV)	(pF)	
TH6P04T6V5CL TH6P04T25CL	50 10	6 24	6.5 25	1 1	12 38	40 40	25 25	500 300	20 pin DILP

(1) MIL STD 883C — Method 3015-2.
(2) Input capacitance : Input pin to ground at 5V bias.