External Memory Address Map

2764(64kbit PROM) or 6264(64kbit SRAM)
ROM / Read / Write (6264 only)
IC11[0000-1FFF] / LT[0000-1FFF] / W[0000-1FFF]
IC12[0000-1FFF] / LT[2000-3FFF] / W[2000-3FFF]
IC18[0000-1FFF] / RT[0000-1FFF] / W[4000-5FFF]
IC19[0000-1FFF] / RT[2000-3FFF] / W[6000-7FFF]

27128(128kbit PROM)
ROM / Read
IC11[0000-1FFF] / LT[0000-1FFF]
IC12[0000-1FFF] / LT[2000-3FFF]
IC11[2000-3FFF] / LT[4000-5FFF]
IC12[2000-3FFF] / LT[6000-7FFF]
IC18[2000-3FFF] / RT[0000-1FFF]
IC19[2000-3FFF] / RT[2000-3FFF]
IC18[0000-0FFF] / RT[4000-5FFF]
IC19[0000-0FFF] / RT[6000-7FFF]

BUS GATE for LS245

Address Auto Increment

Extend ROM chip select

ROM Address Register

Left Read / Write

Left Mirror Read

Right Read/Write

Right Mirror Read

HN6156P C12

HN6156P C14

HN61256P C11
I･O DATA 1

HN61256P C13

DIP28 SOCKET(6264)

DIP28 SOCKET(MCM6264P)

USE AT YOUR OWN RISK

This schematic was written by the trace of the real PCB.
There is no warranty of any kind.

| | | | |
|---|---|---|---|
| Title | KANJI ROM board | | |
| Size: A3 | Number: CZ-8KR/BK2 | Revision: | |
| Date: 2012/11/20 | Time: 17:32:25 | Sheet 1 of 1 | |
| File: CZ_8kr.SchDoc | | | |

www.x1center.org